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Application No. 10/810,204 Amendment dated January 17, 2006 Reply to Office Action of September 15, 2005

Amendments to the Specification:

On page 1 at line 7, please insert the following:

This application is a divisional of U.S. Patent Application No. 10/022,450, filed December 16, 2001, issued on September 13, 2005 as U.S. Patent No. 6,943,529.

Please replace the paragraph beginning on page 22 at line 16, with the following amended paragraph:

FIG. 4 shows an alternate embodiment of a battery charging system 100, which is substantially the same as the battery charging system 10, except that the charging system 100 has an operational amplifier 120, which is used to amplify cutoff voltage V_{R11} (122), and provide amplified and buffered output 124 for use with auxiliary devices. The amplified and buffered output 124 may be used to feed auxiliary devices, such as a microcontroller or indicator device, such as a light emitting diode (LED), or other suitable device. Such auxiliary devices may be programmed in conjunction with other control functions and/or methods, and indicate cutoff voltage. The operational amplifier 120 may be National Semiconductor LM2902 or other suitable operational amplifier. Battery B11 (126) and switch 128 are also shown.

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Please replace the paragraph beginning on page 23 at line 5, with the following amended paragraph:

Voltage 126, which is indicated as V_{sense} below, is simply:

$$V_{\text{scnsc}} = R3/R4 * V_{R11}$$

The resistors should obey the relationship

R3/R4 = R5/R6, where R3, R4, R5, and R6, are the values of the resistors R3, R4, R5, and R6, respectively.

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